



SS 2662

Haratsch 1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#9

Applicant(s): Erich Franz Haratsch,
Harish Viswanathan

Case: 1-4
Serial No.: 09/326,785
Filing Date: June 4, 1999
Group: 2776
Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the
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Signature: *[Signature]* Date: July 17, 2001

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Technology Center 2600

Title: Method and Apparatus for Reducing the Computational Complexity and
Relaxing the Critical Path of Reduced State Sequence Estimation (RSSE)
Techniques

TRANSMITTAL OF FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Attention: Official Draftsperson

SIR:

Applicants submit herewith three (3) sheets of formal drawings in the above-referenced
application.

Respectfully submitted,

Kevin M. Mason

Date: July 17, 2001

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